

# Savvy128 V1.0

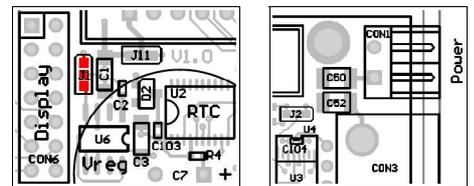
## ATmega128 Controller Board with MMC/SD-Card.



Savvy128 is the microcontroller motherboard for the SavvyBOX application development platform. It provides the Atmel AVR ATmega128 microcontroller, onboard 3.3V voltage regulator, standard RS232 (RS485 optional) serial port with DB9 connector, USB2.0 full speed UART converter, MMC/SD-card header, realtime clock chip with optional GoldCap, push buttons, two-color LED, onboard status LED, infrared detector, expansion headers with all ATmega128 signals and a special header for SavvyDISP, which optionally provides an LC-display, four push-buttons and an LED.

**Components Kit** – Savvy128 comes as a high quality SMT board with all SMT components assembled and all non-SMT components added as a Components Kit, hence the customer can add components and functionality as required by the application. Optionally available is an RS485 kit with MAX485 driver and socket, as well as a GoldCap 1F capacitor, when backing up the realtime clock is necessary. An SD-card is also available as accessory at [www.chip45.com](http://www.chip45.com).

**Power Supply** – Savvy128 provides an external power connector CON1 (see picture right) and a matching screw type cable connector in the components kit. There exist three configurations for supplying the board externally or from the local voltage regulator or from USB.



J11 location

power connector

a) The onboard voltage regulator can be bypassed by setting jumper J11 to position 1-2, which are the upper two red pads in the upper picture on the right. Now the external supply voltage is directly connected to the onboard components, hence it must not exceed  $5V \pm 10\%$ . All onboard components function properly with  $3.3V \pm 10\%$ .

b) By setting jumper J11 to position 2-3 (the lower two pads in the upper right picture), the onboard voltage regulator generates 3.3V from the external supply voltage and drives the whole onboard components. The external supply voltage must be at least 3.7V, to ensure proper regulation of the 3.3V. Since minimum and maximum input voltage depend on current consumption, you should check the voltage regulator datasheet for calculating the exact minimum and maximum voltage levels for your power requirement. The following table shows example configurations.

c) It is also possible to power the board from USB bus power. In that case jumper J11 needs to be set to external power supply (position 2-3) and jumper J3 has to be set according to chapter „USB 2.0 Port“.

Jumper J11	MMC/SD-card wanted	Input Voltage Vin	Onboard VCC	Maximum Current Imax
1-2	no	+5V±10%	+5V±10%	n/a
	yes	+3.3V±10%	+3.3V±10%	n/a
2-3	yes/no	+4V	+3.3V	156mA
		+6V	+3.3V	104mA
		+10V	+3.3V	63mA
		+12V	+3.3V	52mA

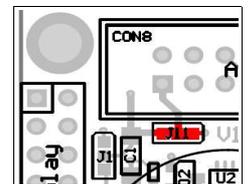
(Maximum current values are given at 25°C/77°F, for higher temperature see datasheet.)

J11 jumper settings and characteristics

Pin 1 of expansion connector CON8 can be connected either to the external input voltage Vin or to the output of the voltage regulator VCC. See table and picture on the right for setting and location of J11. Pad 1 is the right red pad in the picture.

Jumper J11	Voltage at CON8 / Pin1
1-2	local VCC
2-3	external Vin

J11 jumper settings



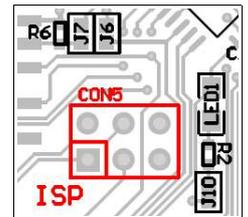
J11 location

**System Clock** – By default the ATmega128 on Savvy128 runs with the internal RC-oscillator set to 8MHz. The frequency of the RC-oscillator can be tuned to 1MHz, 2MHz, 4MHz and 8MHz by changing fuse bits CKSL3..0 with an ISP oder JTAG adapter. See ATmega128 data sheet on page 39 for details.

Additionally to the internal RC-oscillator an external quartz crystal X1 in HC49 package can be assembled beside the ATmega128. See picture on the right for location of X1. The maximum crystal frequency depends on the supply voltage of the ATmega128 (e.g. 0-8MHz @ VCC=2.7-5.5V, 0-16MHz @ VCC=4.5-5.5V), the minimum frequency for a higher frequency crystal is about 400kHz, alternatively a watch crystal with 32768Hz can be connected as X1. In any case, the fusebits CKSEL3..0 and CKOPT need to be set according to the desired oscillator option. See datasheet chapter „System Clock and Clock Options“.

**System Reset** – A proper reset signal is generated onboard by R1, D1 and C6. The internal brown out reset detection of the ATmega128 controller is disabled by default, since it's dependent on the supply voltage.

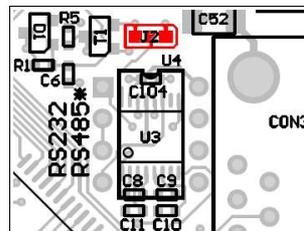
**In-System Programming (ISP)** – CON5 is the standard 6-pin Atmel AVR ISP connector and can be used with most ISP adapters (e.g. stk200/300 compatible adapters like CrispAVR-200 by chip45.com or Atmel's stk500) and software (e.g. uisp, avrdude, PonyProg, etc.). Keep in mind, that the ATmega128 shares the ISP signals PDI/PDO with UART0, hence serial communication might be blocked as long as the ISP adapter is connected.



ISP connector

Be careful when changing fusebits! By selecting an improper combination of fuse bits, it is possible to enter states, where no further ISP programming is possible (e.g. disabling ISP by SPIEN, selecting clock crystal with no crystal assembled or selecting external clock signal with no signal present).

**Serial RS232/RS485 Port** - RS232 level signals are provided by the onboard MAX3221 (U3) RS232 transceiver, connected to UART0 of the ATmega128. U3 can be enabled/disabled by jumper J2 (see picture right for location), which should be set to 1-2 (enabled) or 2-3 (disabled) position before operation. The RS232 signals are available at a standard DB9 male connector CON3.



J2 and RS232 port

DB9 male	RS232 signal	RS485 signal
1	-	-
2	RXD	-
3	TXD	A
4	-	-
5	GND	GND
6	-	-
7	-	-
8	-	B
9	-	-

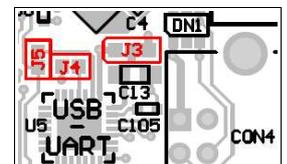
RS232 connector pinout

Optionally a common RS485 transceiver (e.g. MAX485) can be assembled as standard DIL8 device on footprint U4, right above the MAX3221. Be sure to disable the MAX3221 with jumper J2 before assembling the RS485 chip!

The table on the right shows the pinout of the DB9 male connector in both RS232 and RS485 mode. The direction pin DE/RE of the half duplex RS485 transceiver is connected to Pin 4 (signal PE2) of the ATmega128, which can switch between send mode (PE2 = high) and receive mode (PE2 = low).

During ISP programming of the ATmega128 the /RESET signal is asserted and disables the RS232 driver MAX3221 through transistor T1. This avoids the RXD signal of the MAX3221 driving against the ISP signal PDI from the ISP adapter.

**USB 2.0 Port** – Savvy128 provides a USB 2.0 full speed to UART interface based on the CP2102 chip by Silicon Laboratories (www.silabs.com). The USB chip is connected to UART1 of ATmega128 (signals RXD1/TXD1) and converts USB bus data format to UART format at a baudrate of up to 921600 baud. The USB port is externally available at the standard USB-B connector CON4.



USB jumpers

Before you can use the USB port, power needs to be applied to the USB chip via jumper J3. There exist three configurations for Savvy128, which are listed in the table on the right. It is important, that when powering either just the USB chip from USB bus power or powering the whole board from USB bus power, the external Vin voltage must be in the range of 4.0V to 5.25V, due to the USB chip electrical characteristics. Jumper J1 (see chapter Power Supply above) must be set to using the onboard voltage regulator.

Jumper J3	USB Power Configuration
1-2	USB chip is powered from USB bus
2-3	USB chip is powered from Vin (4.0V ≤ Vin ≤ 5.25V)
1-2-3	whole board is powered from USB bus (4.0V ≤ Vin ≤ 5.25V)

USB jumper/power configuration

A royalty-free virtual COM port driver for Windows, MacOSX and Linux is available for download at www.chip45.com. For more information about the USB driver installation see application note AN001 at www.chip45.com/Savvy128.

Savvy128 boards provide a unique USB VID/PID combination and each board additionally has a unique serial number preprogrammed in the internal eeprom of the USB chip. The serial number is also labeled on the bottom side of the board. This ensures that each Savvy128 board always gets assigned the same unique virtual COM port number when connected to the PC, independent of other USB devices or USB hubs. This is a very comfortable feature compared to low-cost USB<->UART interface cables.

**Realtime Clock** – Savvy128 provides a realtime clock chip RTC-8564JE, which is connected via I<sup>2</sup>C bus (SDA/SCL) to the ATmega128 controller (pins PD0/PD1). The RTC-8564 provides one configurable clock output, which can be connected to ATmega128 Timer2 input (pin PD7) by setting jumper J8 to position 1-2, whilst position 2-3 disables the clock output. J8 should be set to either position before operation to ensure a well-defined state of the clock output enable pin of the RTC. The configurable interrupt output of the RTC can be

connected to INT7 of the ATmega128 (pin PE7) by closing jumper J9. Please see the RTC-8564 datasheet (available for download at [www.chip45.com](http://www.chip45.com)) for further details on the clock chip.

If a battery or capacitor backup of the RTC is desired, it is possible to assemble a standard 1F GoldCap right above the RTC chip, see large circle C7 in the picture above. A suitable GoldCap is available as accessory at [www.chip45.com](http://www.chip45.com). Simple charging of the capacitor is done by diode D4 and resistor R4, which limits charging current to 10mA. It is also possible to connect a battery to the pads of C7 instead of the GoldCap. In that case another diode has to be put in series to the batterie, to avoid current flowing into the battery, while the board is powered!

With the standard 1F GoldCap capacitor a backup time of about 140 days at 5V power supply and about 100 days at 3.3V power supply can be expected.

**MMC/SD-Card Interface** – Savvy128 provides a spring-eject socket for MMC/SD-cards. The card is connected internally to the SPI interface of the ATmega128 controller (pins PB0..3, PB0 is chip select output), hence MMC/SD-cards are used in SPI mode. Two jumpers J6 and J7 allow to also route the card's card detect signal and write protect signal to the ATmega128 controller. See table for details.

When MMC/SD-cards are used, the maximum supply voltage for the board is 3.3V, hence a proper external 3.3V supply has to be provided or the onboard voltage regulator has to be used. See chapter on power supply for further information.

**Status LED** – A preassembled small status LED is connected to the ATmega128 OC2/OC1B/PB7 pin. Configuration is low-active, hence the LED flashes, when the pin is set to low. The LED is available after closing jumper J10, see picture on the right for location. J10 is already closed by default, since it is used to indicate proper initialization of the preinstalled bootloader (see below).

**Push Buttons and LED** – Two push buttons and one two-color LED is available as control elements on the front side/panel beside the MMC/SD-card slot. Both buttons and the duo-LED is included in the components kit. See table on the right for connection of the components to the ATmega128 controller and picture for components location.

The buttons and the duo-LED share pins with the SavvyDISP board, hence their usage with SavvyDISP is not recommended!

**Infrared Detector** – An infrared detector can be assembled optionally and provides the possibility to control Savvy128 based application by a standard TV or similar remote control. The default IR detector included in the components kit is a TSOP1736, which is optimized for 36kHz signals, other frequency types can be used alternatively. See the components datasheet for details. Location of the IR detector is shown in the picture on the right. The SavvyCASE front panel and optional SavvyDISP front panel provide a small hole in front of the detector's sensitive area. The output signal of the IR detector is connected to the ATmega128 INT5/PE5 pin.

**Memories** – The ATmega128 provides 128kbytes of onchip, non-volatile Flash memory for program code storage, 4kbytes of onchip application SRAM and additional 4kbytes of onchip non-volatile EEPROM memory.

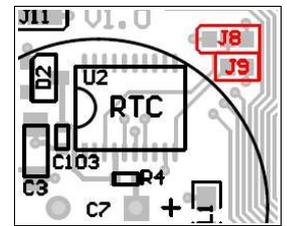
**Fuse Bits** – The ATmega128 fuse bits are preset to the following values: high byte = 0xc8, low byte = 0xdf, ext byte = 0xff. Changes to the factory default are: ATmega103 compatibility mode disabled, OCD and JTAG disabled, CKOPT, preserve EEPROM during chip erase, 8k boot block, boot reset enabled, 4ms startup, high freq. oscillator.

**Bootloader** – The preinstalled bootloader ATmegaBOOT can be used to download application code into the ATmega128 flash memory via either of the UARTs, which means either via RS232 or USB.

After power up, the status of PF7 and PF6 selects if the bootloader is being entered and which UART to be used for download (PF7 pulled low = UART0/RS232, PF6 pulled low = UART1/USB). If the flash memory is empty, i.e.

Jumper J8	clock output
1-2	clock output enabled
2-3	clock output disabled
Jumper J9	interrupt to INT7
closed	interrupt enabled
open	RTC interrupt not used

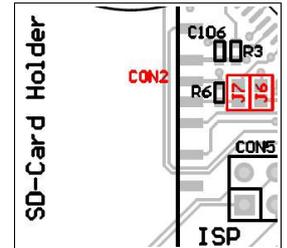
RTC jumper settings



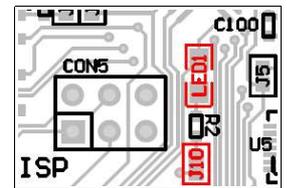
RTC jumper location

Jumper J6	write protect WP
closed	WP connected to PB4
open	WP not used
Jumper J7	card detect CD
closed	CD connected to INT6
open	CD not used

SD-card jumper settings



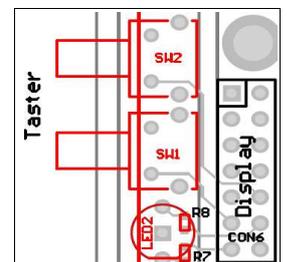
SD card and jumpers



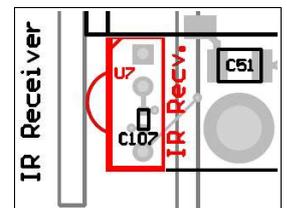
status LED

buttons	ATmega128 pin
SW1	PD4
SW2	PD5
LED	ATmega128 pin
green	PG3
red	PG4

button and LED signals



control elements



IR detector

no application has been programmed yet, the bootloader starts anyway. If neither PF7 or PF6 is set, UART0/RS232 is chosen by default. When the bootloader has started the onboard LED flashes four times for RS232 or five times for USB.

**Note:** The bootloader is compiled for an external clock frequency of 7.3728MHz, hence it is recommended to assemble the included crystal before using the bootloader – using the bootloader with the internal RC-oscillator leads to baudrate errors and might not function properly.

When connecting the Savvy128 to a PC's serial or USB port, the bootloader acts as a standard stk500 ISP adapter, hence most ISP software tools (e.g. AVR-Studio, uisp, avrdude, PonyProg) detect the bootloader as stk500 and can be used in the usual way. Default COM parameters are 115200bps/8N1/no handshake.

The bootloader also provides low-level test features when pressing '!' three times, after the bootloader has started (LED flashed). The LED is switched on, a welcome message is being displayed and the following commands are available: 't' = toggle onboard LED, 'r aaaa' = read byte from memory, 'w aaaa' = write byte, 't' = toggle led, 'b' = external bus loop, 'u' = uart echo and 'j' = jump to application start (0x0000).

```

Tera Term - COM1 VT
File Edit Setup Control Window Help
ATmegaBOOT / Crumb128 - (C) J.P.Kyle, E.Lins - build 041203
t: toggle led
r aaaa:read address
w aaaa: write address
u: uart echo
b: external bus loop
j: jump application
:
    
```

**Note:** The bootloader cannot be used to alter fuse bits, neither is it possible to overwrite or reinstall the bootloader by itself!!! An AVR ISP adapter has to be used for this purpose (e.g. CrispAVR-200 by chip45.com).

**Note:** The JTAG interface is disabled by default, since the bootloader uses PF6/PF7 for initial configuration.

**Expansion Connectors** – Two 26 pin headers CON7 and CON8 provide most of the ATmega128 signals and can be used for expanding the board with customer specific add-on cards. The pinout of the connectors is shown in the following table. The connectors provide standard 2.54mm grid spacing.

**SavvyDISP Connector** – A special 14 pin connector CON6 is located beside the push buttons and LED and provides additional expansion possibilities with one 8 bit port and 4 single bit signals. This connector is primary intended for use with the SavvyDISP LC-display and key add-on board, which perfectly matches to the connector and the guiding rails in the plastic enclosure SavvyCASE (see chapter below). Of course the connector CON6 can be used for other purposes, the pinout is shown in the picture on the right. CON6 is based on standard 2.54mm grid spacing.

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VBUS	1	4	GND																																																																																																																																								
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\* select with jumper J11

**Connector Pinouts**

**Enclosure** – The plastic enclosure Bopla Unimas-85 is compatible with Savvy128 and SavvyDISP and is available as SavvyCASE from chip45.com with fully machined front and rear panels for the onboard connectors and components. SavvyDISP comes with a new front panel, since it requires addition holes for LCD and buttons.



**SavvyCASE**

**Development Tools** – Savvy128 is based on the ATmega128 AVR microcontroller, which can be programmed in assembler (e.g. AVR Studio by Atmel: <http://www.atmel.com/avr>) or with high level languages, such as C/C++, Pascal or Basic. There exist several commercial C/C++ compiler suites as well as the WinAVR GNU C/C++ compiler and tools suite (see <http://winavr.sourceforge.net> for details). A suitable Basic compiler is BASCOM-AVR by <http://www.mcselec.com>. For a Pascal environment visit <http://www.e-lab.de>. Both the AVRco Pascal by E-Lab and the Bascom-AVR provide built-in support for MMC/SD-card.

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